

Overview

The Cisco High-Density Packet Voice Digital Signal Processor (DSP) Module (PVDM2) enables Cisco Integrated Services Routers to provide high-density voice connectivity, conferencing, and transcoding capabilities in Cisco IP Communications solutions.

Cisco PVDM2-8 Features

- Can be configured for high-, medium- or flex complexity via software
- Flex complexity is the default configuration, and in this mode, the network module will dynamically negotiate the appropriate codec (medium or high) on a call-by-call basis
- High complexity codec support - G.723.1, G.728, G.729, G.729b, iLBC, and Modem Relay
- Medium complexity codec support - G.7112, g.729a, G.729ab, G.726, G.722, and Fax Relay

Quick Specs

Figure 1 shows the appearance of PVDM2-8.

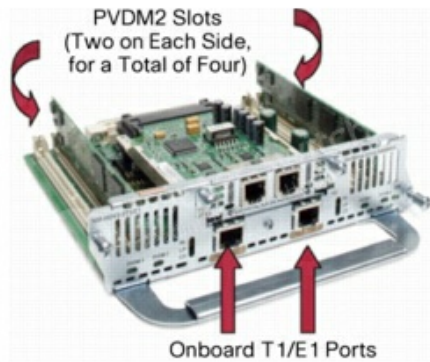


Table 1 shows the Quick Specs.

Product Code	PVDM2-8
Description	8-channel packet fax and voice DSP module
Maximum Number of Channels in G.711	8
Maximum Number of Channels in High-Complexity Codecs	4
Maximum Number of Channels in Medium-Complexity Codecs	4

Product Details

Figure 2 shows PVDM2 SIMM Slots on the Cisco High-Density Digital Voice Network Modules.



Compare to Similar Items

Table 2 shows the comparison of PVDM2-8, PVDM2-32, PVDM2-64, PVDM2-16 and PVDM2-48.

Product Code	PVDM2-8	PVDM2-16	PVDM2-32	PVDM2-64	PVDM2-48
Description	8-channel packet fax and voice DSP module	16-channel packet fax and voice DSP module	32-channel packet fax and voice DSP module	64-channel packet fax and voice DSP module	48-channel packet fax and voice DSP module
Maximum Number of Channels in G.711	8	16	32	64	48
Maximum Number of Channels in High-Complexity Codecs	4	6	12	24	18
Maximum Number of Channels in Medium-Complexity Codecs	4	8	16	32	24

Get more information

Do you have any question about the PVDM2-8?

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Specification

PVDM2 Modules Specifications	
Components	
DSP	• Texas Instruments TNETV2510GGW for PVDM2-16, PVDM2-32, PVDM2-48, and PVDM2-64 • Texas Instruments TNETV2505GGW for PVDM2-8
DSP CPU Clock	• 200 MHz for TNETV2510GGW • 175MHz for TNETV2505GGW
DSP External Memory	128-megabit synchronous dynamic RAM for each DSP (for both TI 2510 and TI 2505)
Interface	80-pin SIMM interface
Features	
Echo Cancellation	Software echo cancellation compliant with ITU-I G.168, with 64-ms tail coverage
Approvals and Compliance	

Safety	• IEC 60950 (worldwide) and AS/NZS 3260 (Australia and New Zealand) • CAN/CSA-C22.2 No. 60950 (Canada) and GB4943-95 (People's Republic of China) • EN60950 (CENELEC; includes EU and EFTA) • SS337 (Singapore; PSB approval), NOM-019-SCFI-1998 (Mexico), and UL 60950 3rd edition (United States)
Homologation	Platform dependent
Mean Time between Failure (MTBF)	System dependent
EMC	
CISPR22, Class B	Emissions
EN55022, Class B	Emissions
CFR47, Part 15, Subpart B, Class B	Emissions
EN61000-3-2	Harmonics
EN61000-3-3	Flicker
CISPR24	Immunity
EN 55024	Immunity
EN50082-1	Immunity
EN61000-3-2	Harmonics
EN 61000-3-3	Flicker
EN 61000-4-2	Electrostatic discharge (ESD)
EN 61000-4-3	Radio frequency (RF) fields
EN 61000-4-4	EFT
EN 61000-4-5	Surge
EN 61000-4-6	Conducted RF
EN 6100-4-8	Power-frequency magnetic fields
EN 61000-4-11	Voltage dips, sags, and interruptions

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